



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Subhash C. Roy et al.

SERIAL NO.: 09/064,474

GROUP ART UNIT: 2783

FILED: April 22, 1998

EXAMINER: Stacy Whitmore

FOR: Real Time Debugger Interface
for Embedded Systems

ATT'Y DOCKET: TRA-040

Honorable Commissioner of Patents
and Trademarks
Washington, D.C. 20231

Sir:

I hereby certify that this correspondence is being deposited
with the United States Postal Service as first class mail in
an envelope addressed to: Commissioner of Patents and
Trademarks, Washington, D.C. 20231 on JAN 19, 2001
David P. Gordon _____ Date
David P. Gordon
Reg No. 29,996

This is a timely reply to an Office Action dated November 20,
2000 (Paper #16). As no claims have been added, and no extension
fee is required, no fees are enclosed herewith.

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AMENDMENT

FEB - 2 2001

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Kindly amend the application as follows:

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In the Claims:

Please rewrite claim 21 as follows.

21. (four times amended) A method of debugging a processor, said
method comprising:

- a) providing information about processor activity in real time;
and
b) associating the instructions executed by the processor with
the information about processor activity, wherein

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